

Pin Assignment Table for Active Signals on the Parallel Port:

PIN#	NAME	I/O (P->S)	I/O (S->P)
1	STROBE*	IN	OUT
2-9	DATA O-DATA 7	IN	OUT
10	ACK*	OUT	NOT USED
11	BUSY	OUT	IN
12	PE	OUT	NOT USED
13	SLCT	PULL HIGH	PULL HIGH
14	----	NOT USED	NOT USED
15	ERROR*	PULL HIGH	PULL HIGH
16	INIT*	IN	OUT
17	SLCTIN*	PULL LOW	PULL LOW
18-25	GROUND	--	--

Pin Assignment Table for Active Signals on the Serial Port:

PIN#	NAME	I/O (P->S)	I/O (S->P)
1	FRAME GROUND	--	--
2	TXD	OUT	OUT
3	RXD	IN	IN
6	DSR	IN	--
7	GND	--	--
20	DTR	--	OUT

(dtc-08/05/93)

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